

FORM PTO-1449  INFORMATION DISCLOSURE STATEMENT BY APPLICANT (USE SEVERAL SHEETS IF NECESSARY)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. MICRON.198C1DV1	APPLICATION NO. 10/614,622
		APPLICANT Li, et al.	
		FILING DATE July 7, 2003	GROUP 2812

U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
<i>m</i>	6,104,633	8/15/00	Abraham, et al.			Feb. 10, 1998
<i>m</i>	6,134,139	10/17/00	Bhattacharyya, et al.			Jul. 28, 1999

FOREIGN PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
						YES NO

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)
<i>m</i>	M. Durlam, et al. "A low power 1Mbit MRAM based on 1T1MTJ bit cell integrated with Copper Interconnects," 2002 Symposium on VLSI Circuits, Digest of Technical Papers (IEEE June 2002), pp. 158-161.
<i>m</i>	M. Durlam, et al., "A 0.18µm 4Mb Toggling MRAM," 2003 IEEE International Electron Devices Meeting, IEDM 2003 Technical Digest (IEEE 2003), pp. 995-997.

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EXAMINER <i>H-Jay Tsai</i>	DATE CONSIDERED <i>3/31/04</i>
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